



**PATENT NUMBER**

**U.S. UTILITY Patent Application**

KC	O.I.P.E.	PATENT DATE
SCANNED <u>LH</u>	O.A. <u>mc</u>	

APPLICATION NO. 09/531026	CONT/PRIOR D	CLASS 703	SUBCLASS 14	ART UNIT 2763	EXAMINER <i>Burda</i> DAY
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Sudheendra Hangal  
James O'Connor

System and method for validating processor performance and functionality

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Assistant Examiner) _____ (Date) _____				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____				<b>ISSUE FEE</b>	
	(Primary Examiner) _____ (Date) _____			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
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